

Docket No.: M&N-IT255

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : KARL SCHROEDINGER
Filed : Concurrently herewith
Title : METHOD AND APPARATUS FOR PRODUCING A CLOCK
OUTPUT SIGNAL



INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent 5,015,872 (Rein), dated May 14, 1991;

Thomas H. Lee et al.: "A 2.5 V CMOS Delay-Locked Loop for an 18 Mbit, 500 Megabyte/s DRAM", IEEE Journal of Solid-State Circuits, Vol. 29, No. 12, December 1994, pp. 1491-1496;

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Werner H. Stemer", is written over a horizontal line.

For Applicant

WERNER H. STEMER
REG. NO. 34,956

Date: November 16, 2001

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